

### ABSTRACT OF THE DISCLOSURE

A data processing system is provided having a processor 46 which generates control signals for controlling further circuits, such as a clock generator 4 and voltage controller 6, to operate so as to support a desired performance level of the processor. Whilst changing between performance levels, the further circuits are capable of supporting intermediate levels of operation and the processor exploits these by operating at those intermediate levels pending the final target level being reached.

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[Figure 5]